REMARKS

Applicants respectfully request reconsideration of the present application in view of the foregoing amendments and in view of the reasons that follow.

Status of Claims:

Claim 18 is currently being cancelled.

Claim 1 is currently being amended.

No claims are currently being added.

This amendment and reply cancels and amends claims in this application. A detailed listing of all claims that are, or were, in the application, irrespective of whether the claims remain under examination in the application, is presented, with an appropriate defined status identifier.

After canceling and amending the claims as set forth above, claims 1-3, 6-12 and 19-20 are now pending in this application for examination on the merits, in which claims 13-17 are withdrawn from consideration as being directed to a non-elected species.

Claim Rejections - Written Description:

In the final Office Action, claim 1 was rejected under 35 U.S.C. § 112, 1st paragraph, as failing to comply with the written description requirement, for the reasons set forth on page 2 of the final Office Action. By way of this amendment and reply, claim 1 has been amended to remove the language in that claim that was alleged to lack sufficient written description support.

Accordingly, presently pending claim 1 is believed to fully comply with 35 U.S.C. § 112, 1st paragraph.

Claim Rejections – Indefiniteness:

In the final Office Action, claim 1 was rejected under 35 U.S.C. § 112, 2nd paragraph, as being indefinite, for the reasons set forth on pages 3 and 4 of the final Office Action. By way of this amendment and reply, claim 1 has been amended to address the 'indefiniteness' issues raised in the final Office Action, by removing the allegedly "indefinite" language from

that claim. Accordingly, presently pending claims 1 is believed to fully comply with 35 U.S.C. § 112, 2nd paragraph.

Claim Rejections - Prior Art:

In the final Office Action, claims 1-3, 6-12 and 18-19 were rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 7,206,003 to Nose et al. ("Nose"); and claim 20 was rejected under 35 U.S.C. § 103(a) as being unpatentable over Nose in view of U.S. Patent No. 5,644,758 to Patrick et al. ("Patrick"). These rejections are traversed with respect to the presently pending claims under rejection, for at least the reasons given below.

Independent claim 1 now recites:

a work memory;

a graphic engine converting externally received image data into first bitmap data, and storing said first bitmap data into said work memory;

a display memory receiving and storing second bitmap data developed from said first bitmap data stored in said work memory; and

a driver circuit which receives said second bitmap data from said display memory, and drives a display panel in response to said second bitmap data received from said display memory,

wherein said first bitmap data includes a plurality of line data each <u>including a</u> <u>plurality of pixel data</u> associated with <u>respective</u> pixels <u>associated with a corresponding gate</u> line of said display panel, and

wherein said data transfer of said first bitmap data from said work memory to said display memory is performed such that each of said <u>plurality of</u> line data is transferred at the same time in parallel from said work memory to said display memory.

The final Office Action asserts that Nose's second selector corresponds to the claimed display memory. This assertion is incorrect. A selector is not used for data storage, and cannot correspond in any way, shape or form to a display memory as recited in claim 1, as such a device is understood in the art.

Thus, for at least this reason, Nose cannot anticipate claim 1.

Still further, with respect to dependent claim 12, that claim recites:

a timing controller controlling said work memory, and said display memory, and said driver circuit,

wherein said driver circuit is connected to said second bit lines, and

wherein <u>said timing controller is adapted to deactivate said display memory</u> to allow said first bitmap data to be transmitted from said work memory to said driver circuit through said second bit lines. (emphasis added).

In its rejection of claim 12, the final Office Action asserts that the dashed line shown in Figure 1 of Nose discloses a timing controller that is adapted to deactivate a display memory to allow first bitmap data to be transmitted from a work memory to a driver circuit through second bits lines. Applicants respectfully disagree. The only dashed lines shown in Figure 1 of Nose is a square-shaped region that delineates the elements making up a controller/driver 2, and a horizontal line that indicates that the output of the Data Line Drive Circuit has many separate output lines. Neither of these features in Figure 1 of Nose is remotely related to features in which a timing controller that is adapted to deactivate a display memory to allow first bitmap data to be transmitted from a work memory to a driver circuit through second bits lines, as recited in claim 12.

Accordingly, dependent claim 12 is patentable over the cited art of record for these additional reasons, beyond the reasons given above for its base claim 1.

Dependent claim 20 recites a transferring means and a displaying means, and it also recites that a first rate at which the first bitmap data is transferred from the work memory to the display memory is faster than a second rate at which the second bitmap data is output from the display memory for display on the display panel. In its rejection of claim 20, the final Office Action asserts that column 2, lines 1-10 of Patrick teaches the features recited in this claim. Applicants respectfully disagree.

Column 2, lines 1-10 of Patrick merely describes that the slower the rate of block transfers of data between memory locations, the slower the rate at which a computer system operates, and that block transfers of data between memory locations should be as fast as possible. This says nothing about having one block transfer rate between a first memory and a second memory and having a second block transfer rate (different from the first block

ransfer rate) between the second memory and another device (e.g., a display). Rather, Patrick would appear to teach having a same, fast transfer rate between all of the devices in his display system, which is totally different from the specific "different speed" features recited in claim 20.

This is essentially the same argument included in the last two-filed responses, in which this argument has not been explicitly addressed in any of the Office Actions issued by the PTO for this application.

Thus, dependent claim 20 patentably distinguishes over the cited art of record for these additional reasons, beyond the reasons given above for its base claim.

Conclusion:

Since all of the issues raised in the final Office Action have been addressed in this Amendment and Reply, Applicants believe that the present application is now in condition for allowance, and an early indication of allowance is respectfully requested.

The Examiner is invited to contact the undersigned by telephone if it is felt that a telephone interview would advance the prosecution of the present application.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing or a credit card payment form being unsigned, providing incorrect information resulting in a rejected credit card transaction, or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

If any extensions of time are needed for timely acceptance of papers submitted herewith, Applicants hereby petition for such extension under 37 C.F.R. §1.136 and authorize payment of any such extensions fees to Deposit Account No. 19-0741.

Respectfully submitted,

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10/03/2010

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